322 D 7880

THIRD ANGLE PROJECTION

From Sht.1

5 WRITE — A – GD

6 COLLATE

7 JUMP IF A=0 — A – GE

8 JUMP

9 JUMP IF A – ve — A – GD

Fns. 10 – 15

50 MATRIX ADDRESS: S0 S4 DB DF

OPERATION	WAVE-FORMS	TIMING
Transfer address from M.reg. into J.reg. and set M reg. to zero.	MTF	*
	DTF1	*
	DTF2	*
	OTG	T1
	VTG	T2
	OTM	T3
	GTJ	T4

51 MATRIX ADDRESS: S1 S4 DB DF

OPERATION	WAVE-FORMS	TIMING
Transfer A. reg. to M. reg. and write contents into above address	ATF	*
	OTG	T1
	FTG	T2
	GTM	T4
	CLEAR	*
	WRITE	*

60 MATRIX ADDRESS: S0 S4 DC DF

OPERATION	WAVE-FORMS	TIMING
Transfer address from M.reg. into J.reg. then read number from that address into M.reg.	MTF	*
	DTF1	*
	DTF2	*
	OTG	T1
	VTG	T2
	OTM	T3
	GTJ	T4
	READ	*
	WRITE	*

61 MATRIX ADDRESS: S1 S4 DC DF

OPERATION	WAVE-FORMS	TIMING
Transfer A. reg. and M. reg. to collate unit then place output of collate unit in A. reg.	MTF	*
	ATF	*
	OTG	T1
	VTG	T2
	OTA	T3
	GTA	T4

70 MATRIX ADDRESS: S0 S4 DA DF

OPERATION	WAVE-FORMS	TIMING
Transfer address from M.reg into Q. reg.	MTF	*
	DTF1	*
	DTF2	*
	OTG	T1
	VTG	T2
	OTQ	T3
	GTQ	T4

71 MATRIX ADDRESS: S1 S4 DA DF

OPERATION	WAVE-FORMS	TIMING
Transfer A. reg. to M. reg. then test for -ve M. reg.	ATF	*
	TM18	*
	OTG	T1
	FTG	T2
	OTM	T3
	GTM	T4

M18=1 / M18≠1

72A MATRIX ADDRESS: S2 S4 CA DA DF

OPERATION	WAVE-FORMS	TIMING
———	—	—

72B MATRIX ADDRESS: S2 S4 CA DA DF

OPERATION	WAVE-FORMS	TIMING
Change M. reg. to 2's complement of present contents then test for -ve M. reg.	MTF	*
	1TF	*
	TM18	*
	OTG	T1
	FTG	T2
	OTJ	T3
	OTM	T3
	GTM	T4

M18=1 / M18≠1

73A MATRIX ADDRESS: S3 S4 CA DA DF

OPERATION	WAVE-FORMS	TIMING
———	—	—

73B MATRIX ADDRESS: S3 S4 CA DA DF

OPERATION	WAVE-FORMS	TIMING
Put address of SCR into J.reg. then write address from Q.reg. into that SCR.	QTF	*
	ETJ	*
	OTG	T1
	FTG	T2
	OTM	T3
	GTM	T4
	CLEAR	*
	WRITE	*

To Sht.2.

80 MATRIX ADDRESS: S0 S4 DD DG

OPERATION	WAVE-FORMS	TIMING
Set J.reg. with address of SCR	ETJ	*
	OTJ	T3

81 MATRIX ADDRESS: S1 S4 DD DG

OPERATION	WAVE-FORMS	TIMING
Write contents of M.reg. into SCR.	CLEAR	*
	WRITE	*

90 MATRIX ADDRESS: S0 S4 DB DG

OPERATION	WAVE-FORMS	TIMING
Transfer address from M.reg. into Q. reg.	MTF	*
	DTF1	*
	DTF2	*
	OTG	T1
	VTG	T2
	OTQ	T3
	GTQ	T4

91 MATRIX ADDRESS: S1 S4 DB DG

OPERATION	WAVE-FORMS	TIMING
Transfer A. reg. to M. reg. then test for -ve M. reg.	ATF	*
	TM18	*
	OTG	T1
	FTG	T2
	OTM	T3
	GTM	T4

M18≠1 / M18=1

92B MATRIX ADDRESS: S2 S4 CA DB DG

OPERATION	WAVE-FORMS	TIMING
———	—	—

92A MATRIX ADDRESS: S2 S4 CA DB DG

OPERATION	WAVE-FORMS	TIMING
Put address of SCR into J.reg. then write address from Q.reg. into that SCR.	QTF	*
	ETJ	*
	OTG	T1
	FTG	T2
	OTM	T3
	GTM	T4
	CLEAR	*
	WRITE	*

Fns. 10–15

TITLE: 920 B CONTROL SEQUENCE FUNCTIONS 0-9.

SHEET 2 OF 2

A11

LATEST M.I INCORPORATED M.I 2998

ISSUE No.	1	2
ALTN. No.	1620	1769
DATE	6.966	29-9-66
INITIALS	S.N.	
PARTS LIST No.		

DRAWN S.NIBLETT | CHECKED CS.707 | APPROVED A.Woods | DATE 30-9-66

MATERIAL | FINISH

© Elliott Brothers (London) Limited
THIS DRAWING IS THE PROPERTY OF ELLIOTT BROTHERS (LONDON) LIMITED AND MAY NOT BE COPIED USED OR DISCLOSED FOR ANY PURPOSE EXCEPT AS AUTHORISED IN WRITING BY ELLIOTT BROTHERS (LONDON) LIMITED

TOLERANCES
DECIMAL ±
FRACTIONAL ±
ANGULAR DIMENSIONS ±
UNLESS OTHERWISE STATED

ELLIOTT BROTHERS (LONDON) LTD

TITLE 920 B CONTROL SEQUENCE. FUNCTIONS 0-9 .

SHEET 2 OF 2

DRAWING No. 322 D 7880